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A yield enhancement methodology for custom VLSI manufacturing. Documents; allows the manufacture of large-area integrated circuits with submicrometer

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Application specific integrated circuit design on

Application specific integrated circuit design on a PC J. Murphy, J. Sumrall, "Custom VLSI Education on a PC Based Platform," ASEE Southeastern Regional

Simultaneous pin assignment and global wiring for

Simultaneous pin assignment and global wiring for custom VLSI design They are integrated and formulated into one special Steiner Circuits and Systems, 1991

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Very-large-scale integration A VLSI integrated-circuit die. Contents. 1 Designers must keep ever more of these rules in mind while laying out custom circuits.

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Custom Integrated Circuits. Series/Report no.: Massachusetts Institute of Technology. Waveform Bounding for Fast Timing Analysis of MOS VLSI Circuits

Full custom - wikipedia, the free encyclopedia

Full-custom design is a methodology for designing integrated circuits by specifying the layout of each individual transistor and the interconnections between them.

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