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(GRNN) based on Field Programmable Gate Array (FPGA). In literature, there are different artificial neural network implementations on FPGA

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Yaghini Bonabi et al. FPGA implementation of a biological neural network. Therefore, FPGA could be an appropriate solution for hardware implementation of neural

The role of the asymptotic dynamics in the design

1. Introduction. In recent years, hardware implementations of artificial neural networks have evolved to VLSI, neuromorphic, FPGA-based and Graphical Processing Unit

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Apr 21, 2013 Neural Network FPGA Implementation The project goal is to develop several IP cores that would implement artificial neural networks using FPGA resources.

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